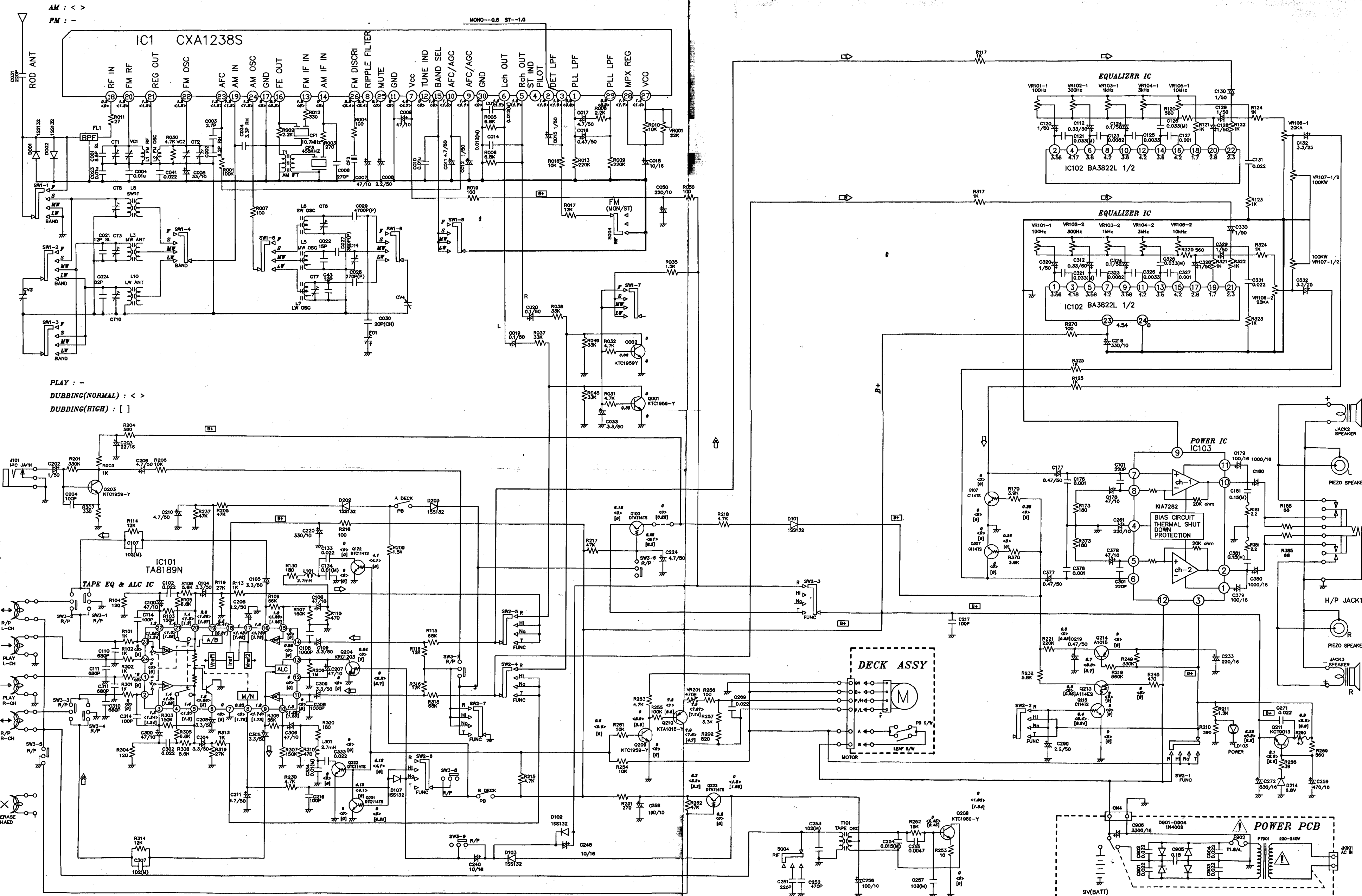


SONY CFS-W 430L SCHEMATIC DIAGRAM  
Note: Design and specifications subject to change without notice.



■ Note on Schematic Diagram ➡

**IC101 TA8189N LOGIC STATES**

PIN	FUNCTION	STATE
5	4-5, 20-21 ON	HIGH
5	4-5, 20-21 OFF	LOW
19	PIN 24/1 INPUT	HIGH
23	PIN 23/2 INPUT	LOW

P/N 495-031N

• All capacitors are on  $\mu\text{F}$  unless otherwise noted pF (or  $\mu\text{F}$ ) 50WV or less are not indicated except for electrolytics and tantalums.  
•  $\square$  Mark: Adjustment for repair.  
• AC Voltage readings in the bias oscillator with a VTVM.  
• Total current measured with no cassette installed.

• Voltage and waveforms are do with respect to ground under no signal conditions.  
• Voltages are taken with a VOM (50k $\Omega$ /V).  
• Voltage variations may be noted due to normal production to tolerances.  
Note: The component identified by mark  $\Delta$  or dotted line with  $\Delta$  are critical for safety. Replace only with part number specified.