

I2S I/O SELECTORS PER 8 CHANNEL

SELECT INPUT SYNC MASTER CLK

PIGGYBACK

CS FOR RES-CHIPS

REVISIONS C (17.01.2001)

DRAWN SP CHECKED DATE 02.04. 2 13:08

PROJECT
AESIO08
DDX3216 EXPANSION BOARD
CNTRL & SELECT
SHEET 1 OF 2 SHEETS TOTAL

